

DEPKREF ROOM-307

MAY 4 - 2001 Technology Centar 2100

JNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Chong Min KYUNG et al.

Serial No.:

09/760,645

Filed:

January 17, 2001

For:

Apparatus And Method For Verifying

A Logic Function Of A Semiconductor Chip

SUBMISSION OF SMALL ENTITY STATEMENT AND REQUEST FOR REFUND UNDER 37 C.F.R. §1.26

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

A Verified Small Entity Statement under 37 C.F.R. 1.9(f) and 1.27(c) for a Small Business Entity is submitted herewith. The verified Small Entity Statement has been filed within three months of the payment of the filing fee in connection with the above identified application.

Applicants submit that the Verified Small Entity Statement was inadvertently omitted at the time the present application was filed.

Applicants therefore request a refund of one half of the U.S. nonprovisional application filing fee (submitted on January 17, 2001) for the above-referenced application. The fee in the amount of \$737.00 should be refunded to Deposit Account No. 02-0200. A duplicate copy of this Request is provided for use by the Deposit

Account Branch.

istment date: 04/27/2001 NVILLARI 19/2001 NROCHA1 00000008 09760<u>6</u>45 BACON & THOMAS
625 Slaters Lane, Fourth Floor

Alexandria, Virginia 22314 04/27/2001 Phone: (703) 683-0500

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Respectfully submitted, **BACON & THOMAS, PLLC**

Attention: FINANCE OFFICE

Eugene Mar Registration No. 25,893 04/27/2001 NVILLARI 0007372800 Name/Number:09760645 \$737.00 CR